

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

1. (Previously Presented) A chip card that receives compressed data fields each preceded by an indication of the expected length of decompressed data and a length of compressed data, comprising a first memory for storing the received fields of compressed data according to the lengths of the respective compressed data, a second memory for storing a decompression algorithm, a processor for decompressing, according to said decompression algorithm, the compressed data in each field into decompressed data over a length depending on the indication of length of decompressed data, and a third memory for storing the decompressed data.

2. (Previously Presented) A chip card according to Claim 1, in which the second memory contains several decompression algorithms, and the processor detects a decompression algorithm number preceding each received field of compressed data so that the compressed data is decompressed in accordance with the decompression algorithm whose number has been detected.

3. (Previously Presented) A chip card according to Claim 2, in which the second memory comprises several decompression models respectively associated with the decompression algorithms, and the processor detects a decompression

model number preceding each received field of compressed data so that the compressed data is decompressed in accordance with the corresponding decompression algorithm and decompression model whose numbers have been detected.

4. (Previously Presented) A chip card according to Claim 2, comprising a fourth memory for storing a decompression model received previously to a received compressed data field, and wherein said processor detects the number of a decompression algorithm preceding said received field of compressed data so that the compressed data is decompressed according to the decompression algorithm whose number has been detected and the decompression model stored in the fourth memory.

5. (Previously Presented) A chip card according to Claim 2, comprising a fourth memory for storing a decompression model implicitly deduced from a compressed data field during writing in the first memory, and the processor detects the number of a decompression algorithm preceding said stored field of compressed data so that the compressed data is decompressed according to the decompression algorithm whose number was detected and the derived decompression model stored in the fourth memory.

6. (Previously Presented) A chip card according to claim 1, wherein said processor detects an indication of the compressed or non-compressed state of each

received data field so that the processor decompresses the data only in the data fields preceded by an indication of a compressed state.

7. (Currently Amended) A protocol data unit for a chip card that receives compressed data fields, comprising a header and a data field, the header including the length of the data field and an indication of the expected decompressed data length after decompression of the data field, wherein the indication of the expected decompressed data length is an n-bit word equal to the expected length of the decompressed data modulo  $2^n$ , the expected length being expressed in decompressed m-bit data words, with n being an integer equal to at least 0 and m being an integer equal to at least 1.

8. (Canceled)

9. (Previously Presented) A protocol data unit according to Claim 7, in which the header further includes the number of a decompression algorithm by means of which the data compressed in the data field is to be decompressed.

10. (Currently Amended) A protocol data unit ~~according to Claim 9, in which the header further includes~~ for a chip card that receives compressed data fields, comprising a header and a data field, the header including the length of the data field, an indication of the expected decompressed data length after decompression of the data field, the number of a decompression algorithm by means of which the data compressed in the data field is to be decompressed, and the

number of a decompression model which corresponds to the decompression algorithm whose number is included in the header and by means of which the compressed data in the data field is to be decompressed.

11. (Previously Presented) A protocol data unit according to Claim 7, in which the header includes a data state indication having a first state when the data in the data field are not compressed, and having a second state when the data in the data field are compressed.

12. (Currently Amended) A protocol data unit ~~according to Claim 11, in which the data state indication has~~ for a chip card that receives compressed data fields, comprising a header and a data field, the header including the length of the data field, an indication of the expected decompressed data length after decompression of the data field, and a data state indication having a first state when the data <sup>n</sup> is the data field are not compressed, a second state when the data in the data field are compressed, and a third state when the data in the data field are to be decompressed in accordance with a predetermined decompression algorithm and a predetermined decompression model. ✓

13. (Currently Amended) A protocol data unit ~~according to Claim 11, in which the header comprises~~ for a chip card that receives compressed data fields, comprising a header and a data field, the header including the length of the data field, a data state indication having a first state when the data in the data field are not compressed and a second state when the data in the data field are compressed, the

number of a decompression algorithm, the number of a decompression model and the indication of the expected decompressed data length after decompression of the data field when the data state indication is at the second state.

14. (Previously Presented) A method for decompressing compressed data fields in a chip card, each compressed data field being preceded by an indication of the expected decompressed data length corresponding to compressed data contained in the field and by a length of the compressed data contained in the field, comprising the following steps:

- detecting the length of the compressed data and storing the compressed data field over the detected length, and
- detecting the indication of the expected decompressed data length and decompressing the data so as to stop the decompression according to the detected indication.

15. (Previously Presented) A method according to Claim 14, further including the steps of selecting a decompression algorithm amongst several decompression algorithms according to the number of an algorithm preceding the compressed data field, and decompressing the data in the field in accordance with the selected decompression algorithm.

16. (Previously Presented) A method according to Claim 15, further including the steps of selecting a decompression model amongst several decompression models associated with the selected decompression algorithm

according to the number of a model preceding the compressed data field, and compressing the data in the field in accordance with the decompression algorithm selected and the decompression model selected.

17. (Previously Presented) A method according to Claim 15, further including the steps of storing a decompression model received previously in the compressed data field, and decompressing the data in the field in accordance with the selected decompression algorithm and the stored decompression model.

18. (Previously Presented) A method according to Claim 15, further including the steps of storing a decompression model deduced implicitly from the received compressed data field, and decompressing the data in the field in accordance with the selected decompression algorithm and the deduced and stored decompression model.

19. (Previously Presented) A method according to Claim 14, further including the step of detecting a data state indication preceding each received data field in order to decompress the data in the field only when the data state indication is not at a first predetermined state.

20. (Previously Presented) A method according to Claim 19, wherein the step of detecting an indication of the expected decompressed data length is not performed when the data state indication is at a predetermined state indicating that

the compressed data are to be decompressed according to a predetermined algorithm and model.